

System-Level EMC/ESD Issues in CMOS ICs

柯明道教授 (Prof. Ming-Dou Ker)

交通大學電子研究所 奈米電子及晶片系統實驗室
Nanoelectronics & Gigascale Systems Laboratory
Institute of Electronics
National Chiao-Tung University, Hsinchu, Taiwan
mdker@ieee.org

Sept. 6, 2007

1

Prof. Ming-Dou Ker

Ker'07

Outlines

1. Introduction to System-Level ESD.
2. Traditional board-level ESD Protection.
3. Transient Noise induced by System-Level ESD.
4. Cases Study.
5. Board-Level Solution with TVS.

Copyright :

*Do not distribute any part of these lecture slides
without the permission of author.*

2

Prof. Ming-Dou Ker

Ker'07