

FARADAY
TECHNOLOGY CORPORATION

No. 5, Li-Hsin Road III, Science-Based Industrial Park,
Hsinchu, TAIWAN, R.O.C.

June 03, 2010

致 柯明道研究副校長/義守大學

主旨: SYSTEM ESD PROTECTION 演講邀請

柯副校長尊鑒:

智原科技公司預定於 **2010/6/18(五)**在新竹智原科技總部，舉辦 System ESD Protection 訓練課程，將通告智原科技公司全體研發及事業部同仁們參加。

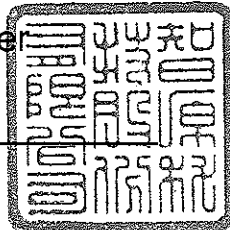
特以此信函邀請您，擔任此訓練課程之專業講師，介紹 System ESD Protection 技術及分享您的專業經驗。

敬請 同意

Faraday Technology Corporation

Name: Wilson Tzang

Title: Chief Operation Officer



Date: June 3, 2010

System-Level Electrostatic Discharge (ESD) Protection in CMOS Integrated Circuits

柯明道教授 (Prof. Ming-Dou Ker), *IEEE FELLOW*

-
- (1) Dept. of Electronics Engineering
National Chiao-Tung University (交通大學), Hsinchu, Taiwan.
 - (2) Dept. of Electronic Engineering
I-Shou University (義守大學), Kaohsiung, Taiwan.
E-mail: mdker@ieee.org

Outline

- Component-Level ESD vs. System-Level ESD
- Some Examples of System-Level ESD Protection in CMOS Chips
- Transient-induced Interference from ESD Gun
- On-Chip Transient-to-Digital Converter
- Summary