

Dear Prof. Ker,

On behalf of 2010 IEEE International Symposium on Next-Generation Electronics (ISNE-2010) Organization Committee, we would like to express our gratitude for your agreement to be our distinguished speaker for this symposium at Taiwan.

However, we would like to express our sincere apology and ask your support again, because the Organization Committee has decided to postpone the conference dates to Nov. 18-19. The reason is because we have been notified that the conference lecture hall will be closed for renovation at the beginning of the coming November. Besides, it is not easy for us to find another appropriate and convenient site nearby due to the price and the number of the expected attendees. Therefore, we appreciate your kind understanding that we have to change the dates of the symposium. (That is, changing from Nov. 3-4 to Nov. 18-19, 2010.)

Except for the conference dates, all other things (length of speech is one hour, feel free to choose speech topic, honorarium, hotel, registration, etc.) remain unchanged.

We apologize again for the inconvenience caused, and please let us know if you are available to give an invited speech at ISNE-2010 on Nov. 18-19 in Kaohsiung, Taiwan. Look forward to hearing from you soon. If possible, please let us know your decision by July 20, 2010.

If you would like to know more about this Symposium, please visit our Web site at <http://www.isne2010.org/>.

Sincerely,

Chua-Chin Wang, Juin J. Liou (General Co-Chairs of IEEE ISNE-2010)

Te-Kuang Chiang, Soon-Jyh Chang (Technical Program Co-Chairs of IEEE ISNE-2010)

Invited Talk in

International Symp. on Next-Generation Electronics (ISNE)

Transient-to-Digital Converter for System-Level ESD Protection Design

Prof. Ming-Dou Ker (柯明道教授), *IEEE FELLOW*

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Nov. 19, 2010

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Outline

- **Component-Level ESD vs. System-Level ESD**
- **System-Level ESD Protection in CMOS Chips**
- **On-Chip Transient-to-Digital Converter**
- **Summary**

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Time	Nov. 18 (Thu.)		
08:00~08:20	Registration & Check In		
08:20~08:40	Opening Remarks		
	(President of NSYSU, General Co-Chairs and Program Co-chairs)		
<div>08:40~9:40</div> 	Keynote Talk 1		
	Topic: Challenges and Trends in Unified Compact Modeling of Conventional (Bulk/SOI) and Emerging (Multigate/Nanowire) MOSFETs		
	Speaker: Prof. Xing Zhou (Nanyang University, Singapore)		
	WEBLINK		
9:40~10:00	Coffee Break		
<div>10:00~11:00</div> 	Invited Talk 1		
	Topic: Current status of compact modeling of nanowire MOSFETs		
	Speaker: Prof. Jin He (Dept. of EECS, Peking University, China)		
	WEBLINK		
<div>11:10~12:00</div> 	Invited Talk 2		
	Topic: Nanotechnology and its application to future semiconductor devices		
	Speaker: Dr. Yoshiji Horikoshi (Waseda University, Japan)		
	WEBLINK		
12:00~13:30	Lunch		
13:30~15:00	Device & Technology	Circuit & System	
	Technical Session 1	Technical Session 1	Poster Session 1
	(5 papers)	(5 papers)	
15:00~15:30	Coffee Break		
15:30~17:00	Device & Technology	Circuit & System	
	Technical Session 2	Technical Session 2	Poster Session 1
	(5 papers)	(5 papers)	
17:00~18:00	NSYSU Campus Tour		

Time	Nov. 19 (Fri.)		
08:30~08:40	Registration & Check In		
08:40~08:50	Award Ceremony		
	(General Co-Chairs and/or Program Co-chairs)		
<div>08:50~10:00</div> 	Keynote Talk 2		
	Topic: Promising Low Power Reusable Solutions for Nano Technology Era:Application Specific Instruction-set Processors		
	Speaker: Prof. Myung Hoon Sunwoo		
	WEBLINK (School of Electrical and Computer Engineering, Ajou Universky, Korea)		
10:00~10:20	Coffee Break		
<div>10:20~11:10</div> 	Invited Talk 3		
	Topic: Emerging Brain-Machine Interfaces for Diagnostic and Recovery of Neural Vital Functions		
	Implantable Biomedical Device for Micro-stimulation and Neural Sensing		
	Speaker: Prof. Mohamad Sawan		
	WEBLINK (Microelectronics and Biomedical Engineering, Ecole Polytechnique de Montréal, Canada)		
<div>11:10~12:00</div> 	Invited Talk 4		
	Topic: Transient-to-Digital Converter for System-Level ESD Protection Design		
	Speaker: Prof. Ming-Dou Ker		
	WEBLINK (Dept. of Electronic Engineering, I-Shou University, Koushiung, Taiwan)		
12:00~13:30	Lunch		
13:30~15:00	Device & Technology	Circuit & System	
	Technical Session 3	Technical Session 3	Poster Session 2
	(5 papers)	(5 papers)	
15:00~15:30	Coffee Break		
15:30~17:00	Device & Technology	Circuit & System	
	Technical Session 4	Technical Session 4	Poster Session 2
	(5 papers)	(5 papers)	